

## A B S T R A C T

A digital signal offset adjusting apparatus has a capacitor (21) causing an output terminal (20b) to pass through a high frequency band of an input digital signal in order to transmit a wideband digital signal without generating a waveform distortion, a first coil (23), one end of which is connected to an input terminal (20a), the first coil passing a low frequency band and a direct current component to another end, a second coil (22), one end of which is connected to an output end, a operational amplifier (31a), a first input end of which is connected to the other end of the first coil, a second input end of which is connected to a direct current voltage generator (25), an output end of which is connected to the other end of the second coil, the operational amplifier outputting to another end of the second coil a signal obtained by subtracting and combining the low frequency band, the direct current component and a direct current bias voltage, and a frequency characteristic compensating circuit (35) connected between a reference electrical potential point and the second input end of the operational amplifier, the compensating circuit being adopted to compensate for a frequency characteristic so that a gain of the operational amplifier increases with a component having a higher frequency from among low frequency bands of the input digital signal passed to

the other end of the first coil.